== Presentation ==

The On Board Computer, or OBC, is the brain of the satellite and has different missions such as the coordination of all the actions, sending orders to the different modules, the reception and storage of information of the CubeSat, sending this information back to Earth via the [http://ece3sat.wikia.com/wiki/Telecommunication\_System\_(TCS) TCS] error handling within the CubeSat.

== Subsystem division ==

The OBC is divided into three subsystems which are Microcontroller, Interfaces and Electronics.

[[File:OBC schematic.jpg|centre|thumb|500x500px|OBC subsystems]]

<p style="text-align:center;">

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The Microcontroller is a processing unit of the OBC dedicated to the Satellite Management (SM). Its has to manage the data from other modules collected through the interface and to use it to give orders.

The interface subsystem is responsible for the communication with other modules. It ensures a good connection with other systems of the satellite and allows them to send information. It is also in charge of collecting the power supplied by the [http://ece3sat.wikia.com/wiki/Electrical\_Power\_Supply\_(EPS) EPS]. The microcontroller is in charge of the storing system data, operation data, logs and measurements.

The electronics subsystem protects all the electronic components of the OBC chip from radiation and magnetic fields. It also prevents destructive noise on the signals processed within the OBC.